

Our F100K family of subnanosecond logic ECL circuits is the fastest in the industry. It's been cited doing 750 ps, which is 2-3 times faster than the competition.

Not only are our components faster, but your systems get faster because we've reduced interconnect delays. Which allows for serial processing with fewer parts.

While being the most complex ECL circuit family available, it's still the easiest to use in system design.

Because it's fully voltage and temperature compensated and has higher integration levels.

Plus, the family offers some important advantages over TTL. Such as lower voltage swing, complementary outputs and a constant power supply current to reduce noise.

## Introducing a new family member— The F220 8-Bit Slice.

Our F100K family includes the F100K SSI/MSI, F200 Gate Array, and a totally new member, the micro-programmed F220 8-Bit Slice family.

With five devices, featuring 1,000-gate density and subnanosecond delays, the 8-Bit Slice family allows for excellent flexibility and building-block versatility over a wide variety of general-purpose applications.

F220 8-BIT SLICE		
Device	Function	Availability
100220	Address and Data Interface Unit (ADIU)	Now
100221	Multi-function Network (MFN)	Now
100222	Dual Access Stack (DAS)	Now
100223	Programmable Interface Unit (PIU)	2nd Q
100224	Microprogram Sequencer (MPS)	4th Q



## Where do you use the F100K family?

Anywhere speed is essential. Especially in large high-performance computers. And right down the line, from mainframes to midis, minis and special processors.

Even in communications and instrumentation equipment.

The F100K family is in production and is available now. More and more design engineers are beginning to design it in every day. In fact, 80% of the companies that build mainframes have already implemented it into their systems in order to stay at the leading edge of technology. With its

faster speed and ease of use, the F100K family has helped increase system performance while decreasing design time.

## Another superior product from the Bipolar technology leader.

The F100K family benefits from Isoplanar-S, Fairchild's evolutionary new Bipolar process for scaling down Isoplanar geometries. With it, we're giving the computer industry and related fields superior memory and logic products. And we'll continue to do so for a long time to come.

If you're not using Fairchild's F100K family, you're wasting both system and design time. And, sooner or later, you'll want to take full advantage of its performance benefits. Why not make it sooner?

For more information about the fastest family in the business, contact your nearest Fairchild office.

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## Shaping the future of LSI technology.